



Serial No. 10/017,171

PATENT
IBM Docket No. RAL920010016US1

I hereby certify that this correspondence (37 CFR 1.97(b)) and attached documents (i.e., PTO 1449 and copies of all cited references) are being deposited with the United States Postal Service as first class mail in an envelope with sufficient postage addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

Karen Orzechowski
Karen Orzechowski

March 6, 2002
Date of Deposit

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re application of : March 6, 2002
J. L. Calvignac, et al. : IBM Corporation - 9CCA/B002
Serial No. 10/017,171 : P.O. Box 12195
Filed: Dec. 7, 2001 : Research Triangle Park,
: North Carolina 27709
: Unit: unknown

For: SEQUENCE-PRESERVING MULTIPROCESSING SYSTEM WITH MULTIMODE
TDM BUFFER

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT WITHIN THREE MONTHS OF FILING OR BEFORE MAILING OF FIRST OFFICE ACTION (37 CFR 1.97(b))

Assistant Commissioner for Patents
Washington, DC 20231
Sir:

The information disclosure statement transmitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever occurs last, under 37 CFR 1.97(b).

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

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The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith. If any fees are due, please charge account 09-1990.

Respectfully submitted,



Joscelyn G. Cockburn
Reg. No. 27,069
Attorney of Record

JGC:ko
919-543-9036
FAX 919-254-2649

In Place of FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANTS' INFORMATION DISCLOSURE
STATEMENT**

Serial No.: 10/017,171
Applicants: J. L. Calvignac et al.
Filing Date: December 7, 2001
Group: Unknown
Atty. Docket No.: RAL920010016US1



Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	5,692,119	11/25/97	Koguchi et al.	395	182.02	4/8/1992
ABA	5,301,333	4/5/94	Lee	395	725	8/27/93
ACA	3,618,040	11/2/71	Iwamoto et al.	711	003	9/17/69
ADA	5,594,918	1/14/97	Knowles et al.	395	800	3/28/95
AEA	4,718,006	1/5/88	Nishida	364	200	12/17/84
AFA	5,021,945	6/4/91	Morrison et al.	364	200	6/26/89
AGA	5,327,419	7/5/94	Clark et al.	370	58.2	7/10/91
AHA						
AIA						
AJA						
AKA						
ALA						
AMA						
ANA						
AOA						
APA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AQA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

ARA J. L. Calvignac et al, U.S. Pending Patent Application Serial Number 09/771,472 filed January 6, 2001, entitled "SINGLE-CYCLE HARDWARE IMPLEMENTATION OF CRYPTO-FUNCTION FOR HIGH THROUGHPUT CRYPTO-PROCESSING

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.